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PATENT APPLICATION  
SERIAL NO. 10/759,936

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Chaudhry et al.

Serial Number: 10/759,936

Filed: January 16, 2004

For: METHOD AND APPARATUS FOR VLSI  
CLOCK GATED POWER ESTIMATION USING  
LCB COUNTS

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Group Art Unit: 2128

Examiner: Herng Der Day

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

**RESPONSE**

In response to the Office Action mailed December 5, 2006 ("Office Action"), Applicants  
respond as follows: